

# Instruction Set Terminology

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Mnemonic (Instruction Name)

**SUBI**

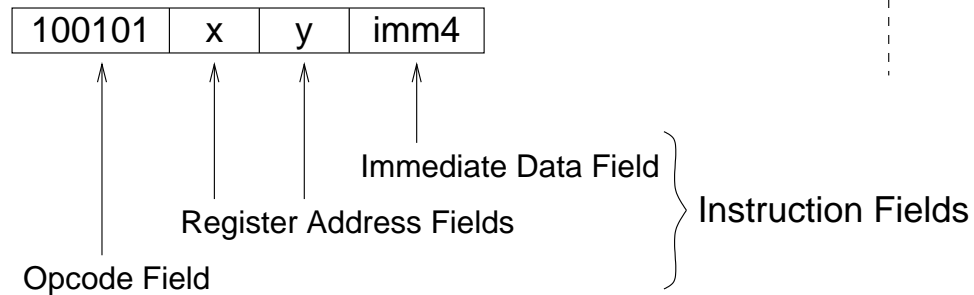
Syntax (Language Structure)    *Assembly Language*

**SUBI Rx, Ry, imm4**

Semantics (Meaning)            *Register Transfer Language*

$Rx \leftarrow Ry - imm4$

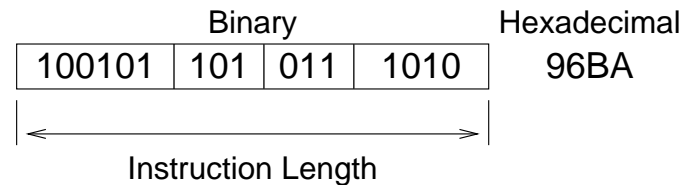
Coding                            *Machine Code*



Example

**SUBI R5, R1, 10**

$R5 \leftarrow R1 - 10$



The *instruction set* for a processor is the set of all the instructions supported by that processor.